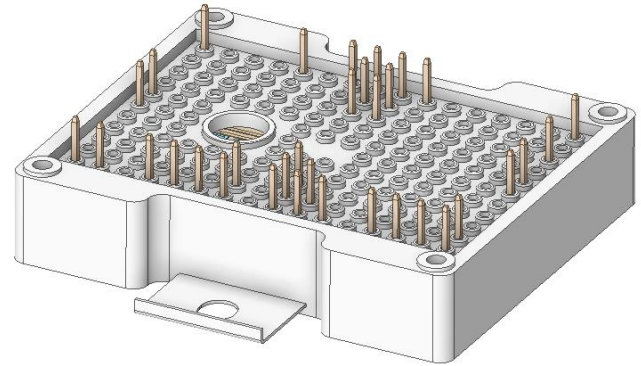


**IGBT - Field Stop,Trench,Soft Fast Recovery Diode**

**650V/160A**

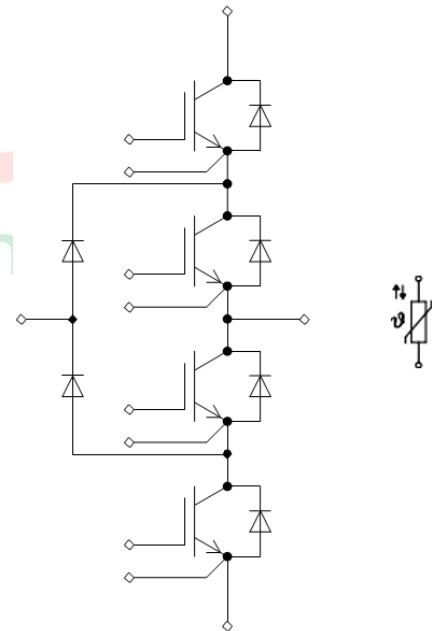
**Features**

- Electrical features
  - $V_{CES} = 650\text{ V}$
  - $I_{C\text{ nom}} = 160\text{ A} / I_{CRM} = 300\text{ A}$
  - Increased blocking voltage capability up to 650 V
  - Low inductive design
  - Low switching losses
  - Low  $V_{CE,sat}$
  
- Mechanical features
  - $\text{Al}_2\text{O}_3$  substrate with low thermal resistance
  - Compact design
  - Rugged mounting due to integrated mounting clamps
  -



**Potential applications**

- Three-level applications
- Solar applications
- UPS systems



Device	Package	Shipping
SPM160V065P2BS	P2B	Tray

**Table 1. ABSOLUTE MAXIMUM RATINGS** (T<sub>J</sub> = 25°C unless otherwise noted) (Note 1)

Rating	Symbol	Value	Unit
<b>IGBT (T1, T2, T3, T4)</b>			
Collector-Emitter Voltage	V <sub>CES</sub>	650	V
Gate-Emitter Voltage Positive Transient Gate-Emitter Voltage (T <sub>pulse</sub> = 5 μs, D < 0.10)	V <sub>GE</sub>	±20 30	V
Continuous Collector Current @ T <sub>C</sub> = 100°C	I <sub>C</sub>	160	A
Pulsed Peak Collector Current @ T <sub>C</sub> = 80°C (T <sub>J</sub> = 175°C)	I <sub>C(Pulse)</sub>	300	A
Maximum Power Dissipation (T <sub>J</sub> = 175°C)	P <sub>tot</sub>	450	W
Minimum Operating Junction Temperature	T <sub>JMIN</sub>	-40	°C
Maximum Operating Junction Temperature (Note 2)	T <sub>JMAX</sub>	175	°C

**IGBT INVERSE DIODE (D1, D2, D3, D4)**

Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	650	V
Continuous Forward Current @ T <sub>C</sub> = 80°C	I <sub>F</sub>	90	A
Repetitive Peak Forward Current (T <sub>J</sub> = 175°C)	I <sub>FRM</sub>	180	A
Maximum Power Dissipation (T <sub>J</sub> = 175°C)	P <sub>tot</sub>	250	W
Minimum Operating Junction Temperature	T <sub>JMIN</sub>	-40	°C
Maximum Operating Junction Temperature	T <sub>JMAX</sub>	175	°C

**NEUTRAL POINT DIODE (D5, D6)**

Peak Repetitive Reverse Voltage	V <sub>RRM</sub>	650	V
Continuous Forward Current @ T <sub>C</sub> = 80°C	I <sub>F</sub>	100	A
Repetitive Peak Forward Current (T <sub>J</sub> = 175°C)	I <sub>FRM</sub>	200	A
Maximum Power Dissipation (T <sub>J</sub> = 175°C)	P <sub>tot</sub>	250	W
Minimum Operating Junction Temperature	T <sub>JMIN</sub>	-40	°C
Maximum Operating Junction Temperature	T <sub>JMAX</sub>	175	°C

Any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

**Table 2. THERMAL AND INSULATION PROPERTIES** (T<sub>J</sub> = 25°C unless otherwise noted) (Note 1)

Rating	Symbol	Value	Unit
<b>THERMAL PROPERTIES</b>			
Operating Temperature under Switching Condition	T <sub>VJOP</sub>	-40 to 150	°C
Storage Temperature Range	T <sub>stg</sub>	-40 to 125	°C
<b>INSULATION PROPERTIES</b>			
Isolation Test Voltage, t = 1 s, 50 Hz (Note 2)	V <sub>is</sub>	4000	V <sub>RMS</sub>
Creepage Distance		6.7	mm
Comparative Tracking Index	CTI	>200	

1. Refer to **ELECTRICAL CHARACTERISTICS, RECOMMENDED OPERATING RANGES and/or APPLICATION INFORMATION** for Safe Operating parameters.
2. **4000 VACRMS** for 1 second duration is equivalent to **3333 VACRMS** for 1 minute duration.

**ELECTRICAL CHARACTERISTICS** ( $T_J = 25^\circ\text{C}$  unless otherwise noted)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit	
<b>IGBT (T1, T2, T3, T4) CHARACTERISTICS</b>							
Collector-Emitter Cutoff Current	$I_{CES}$	$V_{GE} = 0\text{ V}, V_{CE} = 650\text{ V}$	-	-	25	$\mu\text{A}$	
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$	$V_{GE} = 15\text{ V}, I_C = 160\text{ A}, T_J = 25^\circ\text{C}$	-	1.36	1.6	V	
		$V_{GE} = 15\text{ V}, I_C = 160\text{ A}, T_J = 175^\circ\text{C}$	-	1.6	2.0		
Gate-Emitter Threshold Voltage	$V_{GE(TH)}$	$V_{GE} = V_{CE}, I_C = 250\mu\text{A}$	3.7	4.1	4.5	V	
Gate Leakage Current	$I_{GES}$	$V_{GE} = \pm 20\text{ V}, V_{CE} = 0\text{ V}$	-	-	$\pm 1.0$	$\mu\text{A}$	
Turn-on Delay Time	$t_{d(on)}$	$T_J = 25^\circ\text{C}$ $V_{CE} = 300\text{ V}, I_C = 160\text{ A}$ $V_{GE} = -9\text{ V}, 15\text{ V},$ $R_{Gon} = 10\Omega, R_{Goff} = 10\Omega$	-	tbd	-	ns	
Rise Time	$t_r$		-	tbd	-		
Turn-off Delay Time	$t_{d(off)}$		-	tbd	-		
Fall Time	$t_f$		-	tbd	-		
Turn-on Switching Loss per Pulse	$E_{on}$		-	5.9	-		mJ
Turn-off Switching Loss per Pulse	$E_{off}$		-	4.5	-		
Turn-on Delay Time	$t_{d(on)}$		$T_J = 150^\circ\text{C}$ $V_{CE} = 300\text{ V}, I_C = 160\text{ A}$ $V_{GE} = -9\text{ V}, 15\text{ V},$ $R_{Gon} = x\Omega, R_{Goff} = x\Omega$	-	tbd		-
Rise Time	$t_r$	-		tbd	-		
Turn-off Delay Time	$t_{d(off)}$	-		tbd	-		
Fall Time	$t_f$	-		tbd	-		
Turn-on Switching Loss per Pulse	$E_{on}$	-		8.5	-	mJ	
Turn-off Switching Loss per Pulse	$E_{off}$	-		4.8	-		
Input Capacitance	$C_{ies}$	$V_{CE} = 20\text{ V}, V_{GE} = 0\text{ V}, f = 1\text{ MHz}$		-	9674	-	pF
Output Capacitance	$C_{oes}$		-	394	-		
Reverse Transfer Capacitance	$C_{res}$		-	43	-		
Total Gate Charge	$Q_g$		$V_{CE} = 300\text{ V}, I_C = 160\text{ A},$ $V_{GE} = -15\text{ V} \sim 15\text{ V}$	-	320	-	
Thermal Resistance – Chip-to-Heatsink	$R_{thJH}$	Thermal grease, Thickness = $100\mu\text{m} \pm 2\%$ $\lambda = 2.9\text{ W/mK}$	-	tbd	-	K/W	
Thermal Resistance – Chip-to-Case	$R_{thJC}$		-	0.1	-	K/W	

**NEUTRAL POINT DIODE (D5, D6) CHARACTERISTICS**

Diode Forward Voltage	$V_F$	$I_F = 160\text{ A}, T_J = 25^\circ\text{C}$	-	2.1	2.7	V	
		$I_F = 160\text{ A}, T_J = 150^\circ\text{C}$	-	1.9	-		
Reverse Recovery Time	$t_{rr}$	$T_J = 25^\circ\text{C}$ $V_{CE} = 300\text{ V}, I_C = 160\text{ A}$ $V_{GE} = -9\text{ V}, 15\text{ V}, R_G = 9\Omega$	-	tbd	-	ns	
Reverse Recovery Charge	$Q_{rr}$		-	tbd	-	nC	
Peak Reverse Recovery Current	$I_{RRM}$		-	tbd	-	A	
Peak Rate of Fall of Recovery Current	$di/dt$		-	tbd	-	A/ns	
Reverse Recovery Energy	$E_{rr}$		-	tbd	-	$\mu\text{J}$	
Reverse Recovery Time	$t_{rr}$		$T_J = 125^\circ\text{C}$ $V_{CE} = 300\text{ V}, I_C = 160\text{ A}$ $V_{GE} = -9\text{ V}, 15\text{ V}, R_G = 9\Omega$	-	tbd	-	ns
Reverse Recovery Charge	$Q_{rr}$			-	tbd	-	nC
Peak Reverse Recovery Current	$I_{RRM}$	-		tbd	-	A	
Peak Rate of Fall of Recovery Current	$di/dt$	-		tbd	-	A/ns	
Reverse Recovery Energy	$E_{rr}$	-		tbd	-	$\mu\text{J}$	
Thermal Resistance – Chip-to-Heatsink	$R_{thJH}$	Thermal grease, Thickness = $100\mu\text{m} \pm 2\%$ $\lambda = 2.9\text{ W/mK}$		-	tbd	-	K/W
Thermal Resistance – Chip-to-Case	$R_{thJC}$			-	0.17	-	K/W

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
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**IIGBT INVERSE DIODE (D1, D2, D3, D4) CHARACTERISTICS**

Diode Forward Voltage	VF	IF = 90 A, TJ = 25°C	-	Tbd		V
		IF = 90 A, TJ = 150°C	-	Tbd	-	
Reverse Recovery Time	t <sub>rr</sub>	TJ = 25°C VCE = 300 V, IC = 160 A VGE = -9 V, 15 V, RG = 9 Ω	-	Tbd	-	ns
Reverse Recovery Charge	Q <sub>rr</sub>		-	Tbd	-	nC
Peak Reverse Recovery Current	I <sub>RRM</sub>		-	Tbd	-	A
Peak Rate of Fall of Recovery Current	di/dt		-	Tbd	-	A/ns
Reverse Recovery Energy	Err		-	Tbd	-	μJ
Reverse Recovery Time	t <sub>rr</sub>		TJ = 125°C VCE = 300 V, IC = 160 A VGE = -9 V, 15 V, RG = 9 Ω	-	Tbd	-
Reverse Recovery Charge	Q <sub>rr</sub>	-		Tbd	-	nC
Peak Reverse Recovery Current	I <sub>RRM</sub>	-		Tbd	-	A
Peak Rate of Fall of Recovery Current	di/dt	-		Tbd	-	A/ns
Reverse Recovery Energy	Err	-		Tbd	-	μJ
Thermal Resistance - Chip-to-Heatsink	R <sub>thJH</sub>	Thermal grease, Thickness = 100 μm ±2% λ = 2.9 W/mK		-	Tbd	-
Thermal Resistance - Chip-to-Case	R <sub>thJC</sub>		-	0.139 7	-	K/W

**THERMISTOR CHARACTERISTICS**

Nominal Resistance	R <sub>25</sub>	T = 25°C	-	5	-	kΩ
Nominal Resistance	R <sub>100</sub>	T = 100°C	-	490.6	-	Ω

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
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**THERMISTOR CHARACTERISTICS**

Deviation of R25	R/R		-1	-	1	%
Power Dissipation	PD		-	5	-	mW
Power Dissipation Constant			-	1.3	-	mW/ K
B-value		B (25/85), tolerance $\pm 1\%$	-	3435	-	K

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

