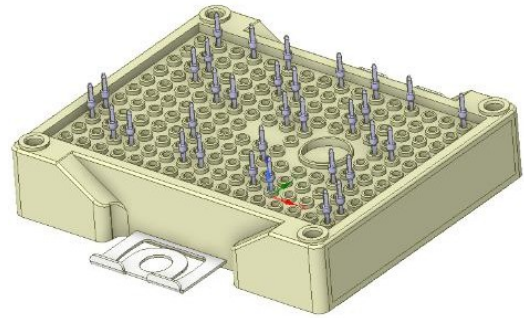


IGBT - Field Stop, Trench, Soft Fast Recovery Diode

650V/150A

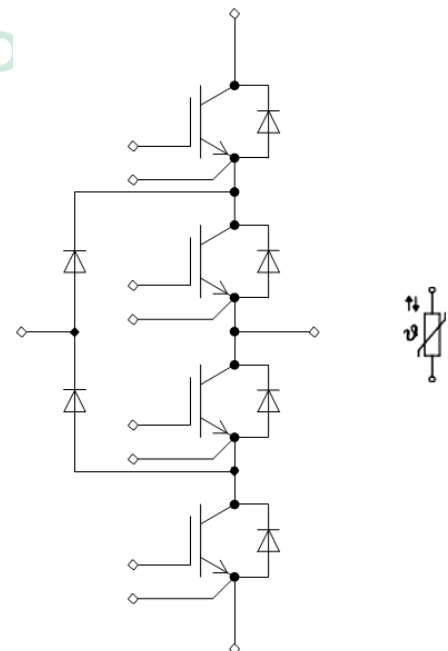
Features

- Electrical features
 - $V_{CES} = 650\text{ V}$
 - $I_{C\text{ nom}} = 150\text{ A} / I_{CRM} = 300\text{ A}$
 - Increased blocking voltage capability up to 650 V
 - Low inductive design
 - Low switching losses
 - Low $V_{CE, sat}$
- Mechanical features
 - Al_2O_3 substrate with low thermal resistance
 - Compact design
 - Rugged mounting due to integrated mounting clamps



Potential applications

- Three-level applications
- Solar applications
- UPS systems



Device	Package	Shipping
SPM150V065P2BS	P2B	TRAY

Table 1. ABSOLUTE MAXIMUM RATINGS (T_J = 25°C unless otherwise noted) (Note 1)

Rating	Symbol	Value	Unit
IGBT (T1, T2, T3, T4)			
Collector-Emitter Voltage	V _{CES}	650	V
Gate-Emitter Voltage Positive Transient Gate-Emitter Voltage (T _{pulse} = 5 μs, D < 0.10)	V _{GE}	±20 30	V
Continuous Collector Current @ T _C = 80°C	I _C	100	A
Pulsed Peak Collector Current @ T _C = 80°C (T _J = 175°C)	I _{C(Pulse)}	200	A
Maximum Power Dissipation (T _J = 175°C)	P _{tot}	350	W
Minimum Operating Junction Temperature	T _{JMIN}	-40	°C
Maximum Operating Junction Temperature (Note 2)	T _{JMAX}	150	°C

IGBT INVERSE DIODE (D1, D2, D3, D4)

Peak Repetitive Reverse Voltage	V _{RRM}	650	V
Continuous Forward Current @ T _C = 80°C	I _F	90	A
Repetitive Peak Forward Current (T _J = 175°C)	I _{FRM}	180	A
Maximum Power Dissipation (T _J = 175°C)	P _{tot}	350	W
Minimum Operating Junction Temperature	T _{JMIN}	-40	°C
Maximum Operating Junction Temperature	T _{JMAX}	150	°C

NEUTRAL POINT DIODE (D5, D6)

Peak Repetitive Reverse Voltage	V _{RRM}	650	V
Continuous Forward Current @ T _C = 80°C	I _F	100	A
Repetitive Peak Forward Current (T _J = 175°C)	I _{FRM}	200	A
Maximum Power Dissipation (T _J = 175°C)	P _{tot}	350	W
Minimum Operating Junction Temperature	T _{JMIN}	-40	°C
Maximum Operating Junction Temperature	T _{JMAX}	150	°C

Any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

Table 2. STORAGE AND INSULATION PROPERTIES (T_J = 25°C unless otherwise noted) (Note 1)

Rating	Symbol	Value	Unit
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STORAGE PROPERTIES

Operating Temperature under Switching Condition	T _{VJO} P	-40 to 150	°C
Storage Temperature Range	T _{stg}	-40 to 125	°C

INSULATION PROPERTIES

Isolation Test Voltage, t = 1 s, 50 Hz (Note 2)	V _{is}	4000	V _{RMS}
Creepage Distance		6.7	mm
Comparative Tracking Index	CTI	>200	

1. Refer to **ELECTRICAL CHARACTERISTICS, RECOMMENDED OPERATING RANGES** and/or **APPLICATION INFORMATION** for Safe Operating parameters.
2. **4000 VACRMS** for 1 second duration is equivalent to **3333 VACRMS** for 1 minute duration.

ELECTRICAL CHARACTERISTICS (T_J = 25°C unless otherwise noted)

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
IGBT (T1, T2, T3, T4) CHARACTERISTICS						
Collector-Emitter Cutoff Current	ICES	V _{GE} = 0 V, V _{CE} = 650 V	-	-	25	μA
Collector-Emitter Saturation Voltage	V _{CE(sat)}	V _{GE} = 15 V, I _C = 150 A, T _J = 25°C	-	1.8	-	V
		V _{GE} = 15 V, I _C = 150 A, T _J = 150°C	-	2.2	-	
Gate-Emitter Threshold Voltage	V _{GE(TH)}	V _{GE} = V _{CE} , I _C = 4 mA	5.0	5.4	5.6	V
Gate Leakage Current	I _{GES}	V _{GE} = ±20 V, V _{CE} = 0 V	-	-	±1.0	μA
Turn-on Delay Time	t _{d(on)}	T _J = 25°C V _{CE} = 300 V, I _C = 150 A V _{GE} = -9 V, 15 V, R _{Gon} = 5 Ω, R _{Goff} = 5 Ω	-	22.4	-	ns
Rise Time	t _r		-	43.2	-	
Turn-off Delay Time	t _{d(off)}		-	155.2	-	
Fall Time	t _f		-	32	-	
Turn-on Switching Loss per Pulse	E _{on}		-	2.42	-	
Turn-off Switching Loss per Pulse	E _{off}	-	2.45	-		
Turn-on Delay Time	t _{d(on)}	T _J = 150°C V _{CE} = 300 V, I _C = 150 A V _{GE} = -9 V, 15 V, R _{Gon} = 5 Ω, R _{Goff} = 5 Ω	-	23.2	-	ns
Rise Time	t _r		-	45.5	-	
Turn-off Delay Time	t _{d(off)}		-	167	-	
Fall Time	t _f		-	36.2	-	
Turn-on Switching Loss per Pulse	E _{on}		-	3.65	-	
Turn-off Switching Loss per Pulse	E _{off}	-	3.82	-		
Input Capacitance	C _{ies}	V _{CE} = 20 V, V _{GE} = 0 V, f = 1 MHz	-	8485	-	pF
Output Capacitance	C _{oes}		-	610	-	
Reverse Transfer Capacitance	C _{res}		-	70	-	
Integrated Gate Resistor	R _g		-	2	-	Ω
Thermal Resistance - Chip-to-Case	R _{thJC}		-	0.24	-	°C/W

NEUTRAL POINT DIODE (D5, D6) CHARACTERISTICS

Diode Forward Voltage	V _F	I _F = 150 A, T _J = 25°C	-	1.5	1.95	V
		I _F = 150 A, T _J = 150°C	-	1.3	-	
Reverse Recovery Time	t _{rr}	T _J = 25°C V _{CE} = 300 V, I _C = 150 A V _{GE} = -9 V, 15 V, R _G = 5 Ω	-	123	-	ns
Reverse Recovery Charge	Q _{rr}		-	4312	-	nC
Peak Reverse Recovery Current	I _{RRM}		-	70	-	A
Peak Rate of Fall of Recovery Current	di/dt		-	2.757	-	A/ns
Reverse Recovery Energy	E _{rr}		-	675	-	μJ
Thermal Resistance - Chip-to-Case	R _{thJC}		-	0.37	-	°C/W

Characteristic	Symbol	Test Conditions	Min	Typ	Max	Unit
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IIGBT INVERSE DIODE (D1, D2, D3, D4) CHARACTERISTICS

Diode Forward Voltage	V_F	$I_F = 90\text{ A}, T_J = 25^\circ\text{C}$	-	1.35		V
		$I_F = 90\text{ A}, T_J = 150^\circ\text{C}$	-	1.13	-	
Reverse Recovery Time	t_{rr}	$T_J = 25^\circ\text{C}$ $V_{CE} = 300\text{ V}, I_C = 150\text{ A}$ $V_{GE} = -9\text{ V}, 15\text{ V}, R_G = 5\ \Omega$	-	123	-	ns
Reverse Recovery Charge	Q_{rr}		-	4312	-	nC
Peak Reverse Recovery Current	I_{RRM}		-	70	-	A
Peak Rate of Fall of Recovery Current	di/dt		-	2.757	-	A/ns
Reverse Recovery Energy	E_{rr}		-	675	-	μJ
Thermal Resistance – Chip-to-Case	R_{thJC}		-	0.37	-	$^\circ\text{C/W}$

THERMISTOR CHARACTERISTICS

Nominal Resistance	R_{25}	$T = 25^\circ\text{C}$	-	5	-	$\text{k}\Omega$
B-value	$B_{25/50}$	B-value ($25^\circ\text{C}/50^\circ\text{C}$), tolerance $\pm 2\%$	-	3375	-	K

Product parametric performance is indicated in the Electrical Characteristics for the listed test conditions, unless otherwise noted. Product performance may not be indicated by the Electrical Characteristics if operated under different conditions.

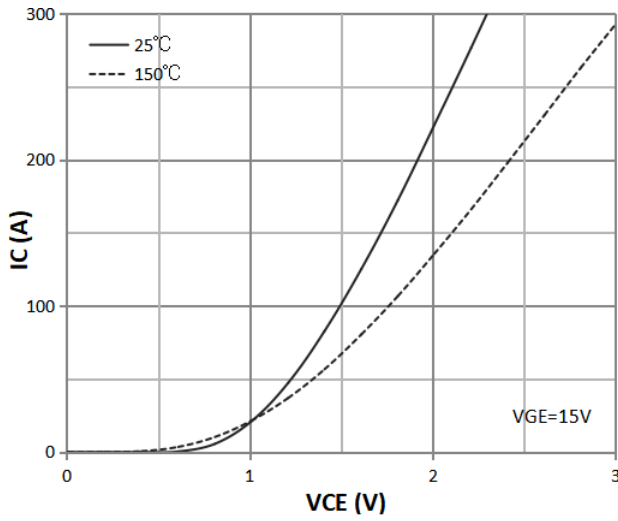


Figure 1. Typical Output Characteristics IGBT

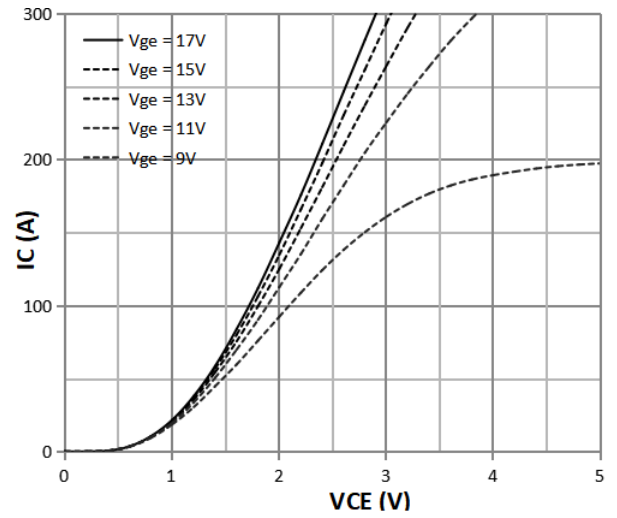


Figure 2. Typical Output Characteristics IGBT

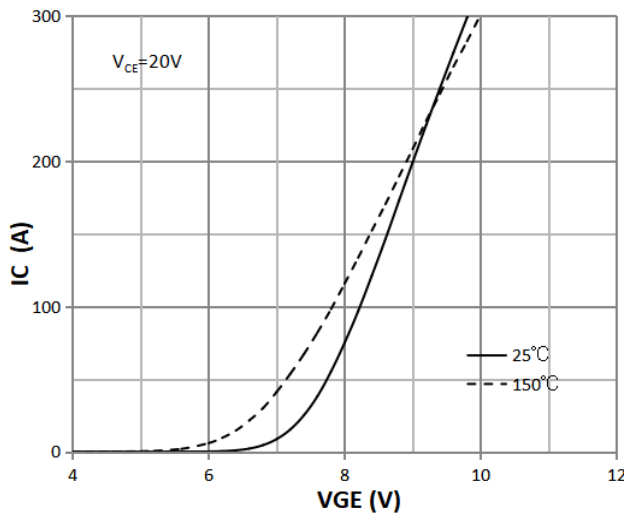


Figure 3. Typical Transfer Characteristics IGBT

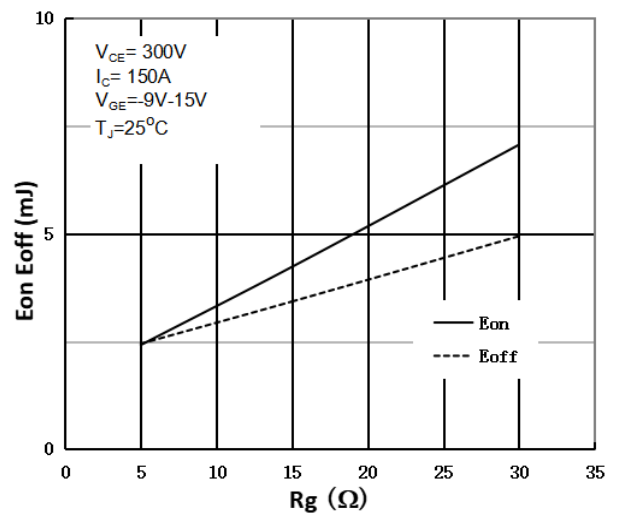


Figure 4. Switching Energy vs Gate Resistor IGBT

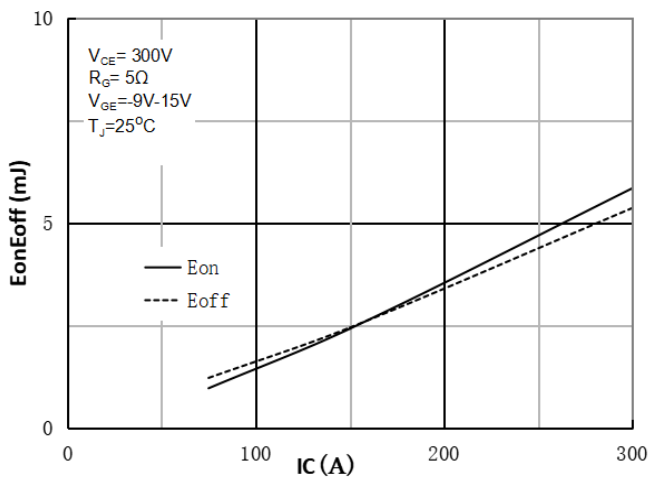


Figure 5. Switching Energy vs Collector Current IGBT

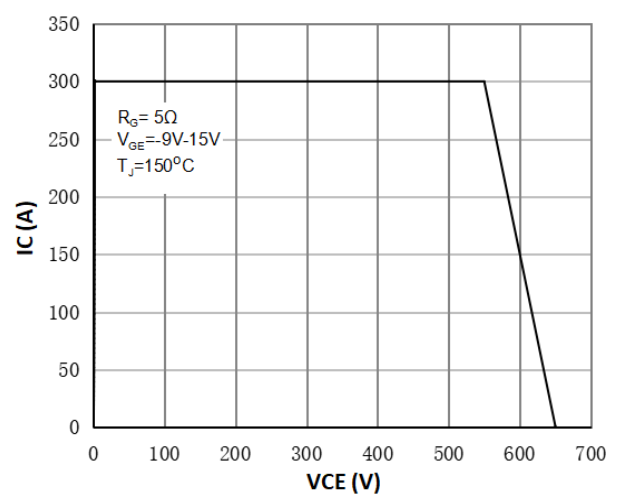


Figure 6. Reverse Biased Safe Operating Area IGBT

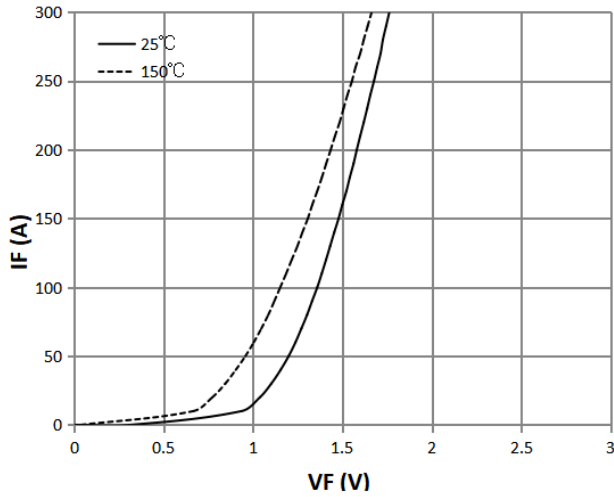


Figure 7. Diode Forward Characteristics

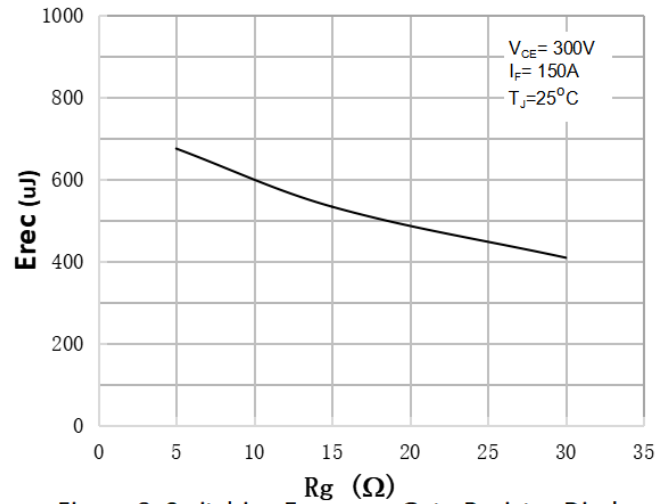


Figure 8. Switching Energy vs Gate Resistor Diode

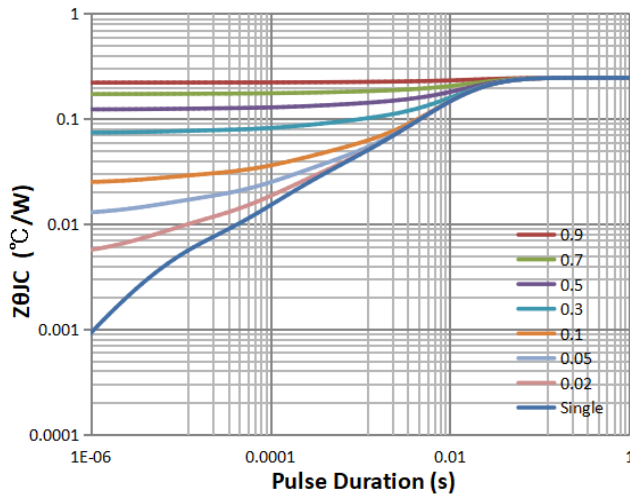


Figure 9. IGBT Transient Thermal Impedance, Junction to Case

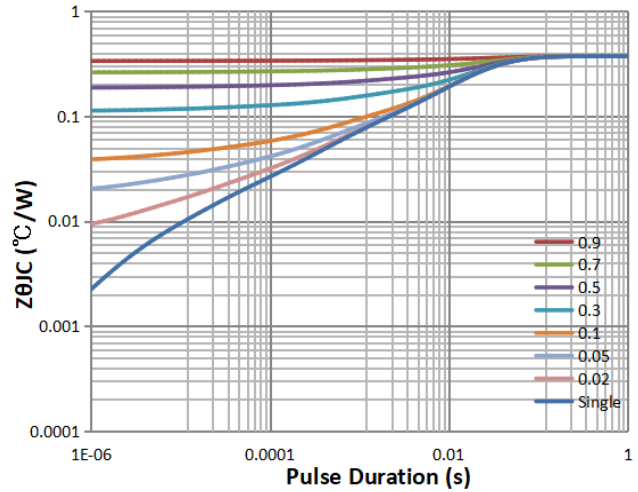


Figure 10. Diode Transient Thermal Impedance, Junction to Case

